

Prof. Sumita Gupta

Qualification M.Tech (VLSI Design)

Mail id: sumita.gupta@kccemsr.edu.in

Objective

To optimize the utilization of my technical and professional skills to nurture my career to the peak of responsibilities, and always be a learner to everything new.

Teaching Experience

Institute /Organization	Position Held	Period of appointment
K.C College	Assistant Professor	12th Feb 2024 to till now
K.C College	Assistant Professor	28 th April 2014 to 15 th November 2021
H.I.T.M Agra	Assistant Professor	5 th Feb 2007 to 22 th Nov 2013

Qualification

Examination	University	Month & Year of Passing	Subject & Specialization	Class
M. Tech	U.P.T.U Lucknow	2012	VLSI Design	FIRST
B. Tech	U.P.T.U Lucknow	2006	Electronics & Communication	FIRST
XII	U.P. Board	2002	SCIENCE	FIRST
X	U.P. Board	2000	SCIENCE	FIRST

Membership of professional bodies

Sr.No	Name of Professional Body	Status/ Number
1	ISTE	LM64996

Subject Taught:

Month/Year	Subject Taught(Semester)	Labs Handled
2007	Semiconductor Material & Devices	YES
2008,2009,2010	Control System, Digital Signal Processing , Digital Communication	YES
2011	Microprocessor ,Digital Signal Processing	YES
2012	Integrated Circuit, VLSI Design	YES
2013	Electronic Circuits	YES
2014	VLSI, DLIC (design with Linear Integrated Circuit), PCS(Principle of Control System)	YES
2015	Power Electronics & Drives, VLSI Design	YES
2016	Power Electronics, Control System, Linear Integrated Circuit	YES
2017-18	Electronics Instrumentation & Control System, IC Technology ,VLSI Design	YES
2018-19	Electronics Instrumentation & Control System, IC Technology ,Digital VLSI Design	YES

2019-20	Electronic Devices & Circuit I ,Electronic Devices & Circuit II,OSTL(open source Technology lab)	YES
2020-21	EDC, DVLSI, Internet Communication Engineering	YES

Publications

Conferences (International), Conferences and Journals (National)

- **Sumita Gupta**, Rishikesh Singh, Akshata Nikam, Pravin More, Aditya Gangurde “IR Infrared Based Remote Controlled Industrial/Home automation using Arduino” GIS Science Journal, Volume VIII, Issue 4, April 2021, ISSN NO -1869-9391
- **Sumita Gupta**, Aditya Jadhav, Chetna Ghadigaonkar, Saloni Thale, Tejal Temkar “IOT based flood monitoring & Bridge Crack Detection System” GIS Science Journal, Volume VIII, Issue 5, April 2021, ISSN NO -1869-9391
- **Sumita Gupta**, Hariharan Nadar, Vikas Rajak, Kirti Lokhande, Shalini Pandey,”Covid 19 Prevention & Detection System for College Campus”, published in IJIREEICE, volume IX, Issue V, May 2021
- **Sumita Gupta** , Christina Bage, Purnima Vadak , ” analysis of semiconductor materials based on average electron energy & drift velocity” published in Volume 22,No1,2020 of our Heritage Journal with ISSN 0474-9030
- **Sumita Gupta**, Sushrut Ahire, Akash Pandya, “Digital Notice Board & Lab automation using Raspberry Pi” GIS Science Journal, ISSN NO -1869-9391
- **Sumita Gupta**, Tanvi Upaskar , Ganesh Thorave, Mayuresh Yednekar, Siddhesh Mahadik, “Analysis of Various memory circuits used in digital VLSI” in international journal of VLSI Design, microelectronics & Embedded system,Volume IV ,Issue 1.
- **Sumita Gupta**, Siddhesh Kadam ,Sidhika Vijayvergiya, Aishwarya Phadtare,Chandrashekhar Kanavali, “ ATM Security System”, in International Journal of Latest Technology in Engineering, Management & Applied Science (IJLTEMAS) Volume VIII, Issue IV, April 2019 | ISSN 2278-2540 (pp78-82).
- **Sumita Gupta**, Adala Rajesh babu ,Sahil Borade, Vimal Maurya,Sunil Swarnkar, “SMART AGRIBOT” in International Journal of Latest Technology in Engineering, Management & Applied Science (IJLTEMAS) Volume VIII, Issue IV, April 2019 | ISSN 2278-2540 (56-59)
- **Sumita Gupta**, Prerna Pisal , Aditya Malviya, Abbas, Ankita Paste, “Analysis of various adders used in VLSI ”, in third international conference on advanced trends in Engineering(ICATE 2019) held on 4th-5th April 2019 at Datta Meghe College of Engineering Airoli.

- **Sumita Gupta**, Rahul Raje , Harminder S, Chaitali S, Aatreya Sawant, “VLSI Prevention & protection Circuits” in third international conference on advanced trends in Engineering (ICATE 2019) held on 4th-5th April 2019 at Datta Meghe College of Engineering Airoli.
- **Sumita Gupta** ,Christina Bage, Purnima vadak, “Analysis of Crystalline Solar cell using drift diffusion model & P-V Analyzer”, in second international conference on Multidisciplinary Research held on 8th -9th September 2018 at Maharashtra Chamber of Commerce ,Industries & Agriculture Tilak Road ,Pune (India) & published in International Journal of Management Technology and Engineering ,An ISO :7021-2008certified journal (ISSN No. 2249-7455) ,volume 8 ,issue IX ,September 2018
- **Sumita Gupta**, et al “Modelling of different carbon nano tube structure (CNT) & its application” in proceeding of international conference on Advance in Science ,Technology & Engineering (ICASTE 2018) held on 20th - 21st April 2018.
- **Sumita Gupta**, et al “ Implementation of Bottle filling & Capping system using PLC with SCADA” in international conference on New Era in Technologies ,Science & Role of Management held on 9th -10th April 2018 at Parvatibai Genba Moze College of Engineering ,Wagholi ,Pune & published in International Journal of Advance Research in Science & Engineering (ISSN No 23198354),volume no 7 ,Special issue no 04 ,April 2018.
- **Sumita Gupta**, et al, “An Automatic System for monitoring Aquaculture in digital rural India on 4th -5th March 2017 , ICEMTE at L.R. Tiwari College of Engineering Mira Road
- **Sumita Gupta**, et al" Power Optimization for Low power VLSI circuits " proceeding of International Conference on Electronics and Computing Technologies (ICONECT'16) at K. C. College of Engineering and Management Studies and Research on 10th – 11th March 2016 and published in International Journal IJARCSSE (ISSN no - 2277 128X)
- Christina B, Purnima V & **Sumita G** “Gain Flattening of a Wideband, High Gain MSA by Varying Dimensions and Spacing of Parasitic Patches”, International Conference on Electronics and Computing Technologies (ICONECT'15) at K. C. College of Engineering and Management Studies and Research on 10th – 11th March 2015 and published in International Journal IJARCSSE (ISSN no - 2277 128X)
- Priyanka Yadav, **Sumita Gupta** ,Gaurav Kumar “ Design and Implementation of 4-Bit Arithmetic and Logic Unit Chip with the Constraint of Power Consumption” IOSR Journal of Electronics and Communication Engineering (IOSR-JECE) e-ISSN: 2278-2834,p- ISSN: 2278-8735.Volume 9, Issue 3, Ver. V (May - Jun. 2014), PP 36-43

- **Sumita Gupta** ,Vikas Maheswari, Rajib Kar, V.Satyanarayan and A.K. Bhattacharjee, “Estimation of RC Global Interconnect Slew in 0.18 μm Technology using Inverse Gamma Distribution Function”, in Proc. of International Conference on Network Communication and computer , New Delhi, India, pp. 224-228, 2011.
- **Sumita Gupta** ,Vikas Maheswari , Rajib Kar, V.Satyanarayan and A.K. Bhattacharjee, “Delay Estimation for Global RC Interconnect using Inverse Gamma Distribution Fuction, in Proc. of International Conference on Network Communication and computer , New Delhi, India, pp. 220-223, 2011.
- **Sumita Gupta** ,Vikas Maheswari , Rajib Kar, D. Mandal, A.K. Bhattacharjee, Modelling of crosstalk and delay for Distributed RLCG On-Chip Interconnects For Ramp Input, in proc. of Second International Joint Conference on Advances in Engineering and Technology AET 2011, Noida, India, pp. 10-14 , Dec 20-21, 2011.

Sumita Gupta ,Vikas Maheswari , V.Satyanarayan, Rajib Kar, D. Mandal, A.K. Bhattacharjee, “Inverse Gamma Distribution based Delay and Slew Modeling for On- Chip VLSI RC Interconnect for Arbitrary Ramp Input”, in proc. of Second International Joint Conference on Advances in Engineering and Technology AET 2011, Noida, India, pp. 6-10 , Dec 20-21, 2011

STTPs,FDPs, Workshop, Seminar, Courses attended

- **Faculty Development Programme (FDP-06)** at Gr. Noida(2006)
- Workshop on **Matlab For Control System** organized by B.M.A.S Engineering College Agra(2007)
- Workshop on **Professional Communication** in 2008 at BMAS Agra
- Workshop on **Human resource Development Intervention-FEEL Teacher** Conducted by CLHRD,Manglore (2008)
- Workshop on **Human Values & Professional Ethics** at 2010 in BMAS Agra
- Workshop on **LATEX 2012** conducted by CS & IT Department in HITM Agra
- National Workshop on **Microwave Engineering** in H.I.T.M Agra(2012)
- Workshop on **AKASH Tablet** (2012) in H.I.T.M Agra
- International Workshop on **DSP & Its Real Time Application** in AEC Agra(2009)
- Two week **ISTE** workshop on **Analog Electronics** conducted by **IIT Kharagpur(2013)**
- Workshop on **LATEX 2013** conducted by research development cell in HITM Agra
- **Faculty Development Programme** at KCCEMSR on “ **Next Generation Technologies in Electronics & Telecommunication**” (16 December -22 December 2016)
- AICTE & ISTE approved FDP on “**Design of Internet of Things & Microwave Circuits & Systems at KCCEMSR,Thane**” on 18/12/2017- 23/12/2017
- AICTE & ISTE approved STTP on “**Open Data Science & IOT at Vidhyalankar Institute of Technology ,Wadala**” on 25/6/2018 to 30/6/2018
- **6 days online FDP on “Python 3.4.3” organized by** RMD Sinhgad School of Engineering, Warje, Pune, in association with spoken tutorial IIT Bombay on **20th -25th April 2020.**

- **3 days online workshop on “Understanding Consumer (Student) Behaviour and its Impact on Delivery of Lectures “organized by Model College Mumbai on 26/4/2020**
- **3 days online workshop on "Education 4.0" organized by IQAC ,Atherva College of Engineering on 26/4/2029-30/4/2020.**
- 3 days Webinar on “**Happiness & Harmony**” organized by Human Value Education Cell in collaboration with Department of H&AS of KCCEMSR Thane on 27/4/2020-29/4/2020
- 6 days online FDP on “**Latex**” **organized by** A.P. Shah Institute of Technology in association with spoken tutorial IIT Bombay on **27th April -2nd May 2020**
- 2 days webinar on Combating Corona with yoga & Meditation organized by Human Value Education Cell in collaboration with Department of H&AS of KCCEMSR on **29th -30th April 2020.**
- 6 days online FDP on “**Moodle**” **organized by** Sinhgad Institute of Technology,Lonavala in association with spoken tutorial IIT Bombay on **1st May-6th May 2020.**
- 4 day webinar on “Block chain Technologies for small contracts” organized by Thandomal Shahani Engineering College on **3rd -6th May 2020.**
- 6 days online FDP on “SCI Lab” organized by S. B. Jain Institute of Technology Management and Research, Nagpur in association with spoken tutorial IIT Bombay on **4th -9th May 2020.**
- FDP on “**Research, Funding and IPR**” under the banner of IETE and IIC organized by K.C College of engineering & Management studies & Research Thane on **7th -10th May 2020.**
- Webinar on “**IOT**” organized by Universal College of Engineering on 14th-15th May 2020.
- Completed **ATAL** online FDP on “**Quantum Computing**” from 1/6/2020-5/6/2021
- Completed **ATAL** online FDP on “**IOT**” from 23/2/2021-27/2/2021
- Completed **ATAL** online FDP on “**Blended Learning & Flipped Classroom**” from 8/2/21021-12/2/2021
- Completed **ATAL** FDP on “**Robotics**” from 11/1/2021-15/2/2021
- Completed 1 day workshop on “**SEQUEL App**” organized by Bhartiya Vidhya Bhavan’s Sardar Patel Institute of Technology on 16 Jan 2021
- Organized 1 week STTP on “**VLSI Design from Basic to Advance**” from 25/1/2021-30/1/2021 in KCCEMSR Thane
- Attended Webinar on “**Issues & Challenges in filling Patents**” organized by KKR & KSR Institute of Technology held on 25/7/2021

(Other achievements)

- “**IARDO Award for young scientist 2018**”
- Qualified in **GATE-2019** with score **411(90 Percentile)**
- Qualified in **GATE-2011** with score **434(95 Percentile).**
- Qualified in **GATE-2012** with score **366 (90 Percentile).**
- **Certificate for outstanding performance for the year 2010-2011 as a faculty member of H.I.T.M Agra (CPI 85.7)**
- Received **Chairperson’s Recognition award**

Extra-Curricular Activities:

- Created own blog of poems “sumitapoem.blogspot.com”
- Created our own YouTube channel

STTPs, FDPs, Workshop, Seminar, Conferences organized

- Organized 1-week STTP on “**VLSI from Basic to Advance**” under VLSI Learning Centre in association with IETE (25th January-30th January 2021)
- Organized value-added course on “**VLSI Front & Backend Design**” under VLSI Learning Centre (2020-21)
- Organized intercollegiate “**Online GATE awareness Test series for EC**” (21st January 2021-4th February 2021) under IETE
- Conducted workshop on “Introduction to VLAB & its utilization” in Sahyog College of management Studies along with VLAB team .

Mrs. Sumita Gupta